Docket No.: 03-2040 PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Jeffrey Heidel By the Examiner:

Serial No.: 10/695,880

:

Filed: October 28, 2003

Group Art Unit:

Title: METHOD FOR IMPROVED TRACEABILITY OF COMPONENTS USED IN MANUFACTURING OF A PRINTED CIRCUIT BOARD (PCB)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service on December 12, 2003, in a First Class envelope, with sufficient postage thereon, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Mikele I. Novine

Michele D. Norine

DATED: December 12, 2003

Dear Sir:

## INFORMATION DISCLOSURE STATEMENT

Applicant submits herewith patents, publications or other information of which he is aware, which he believes may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98(a)(1).

A copy of each of these items, publications or other information, on PTO-1449 is supplied herewith.

Please direct all correspondence and telephone calls to:

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DATED:

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